

Session: TH1A

Low Noise Devices and Circuits

Chair

M.S. Gupta

Hughes Aircraft

Co-Chair

M. Pospieszalski

NRAO

Methods of low-noise MMIC design are being extended to meet such goals as small size, low cost, and high linearity. The variety of active devices available for low noise applications is being enlarged through extending the range of use for existing low-noise devices as well as through redesign of devices like HBTs which have demonstrated other advantages besides low noise. New designs for passive components, such as circulators and floating inductors, are being carried out for receiver and other low-noise applications.

8:00 am - 9:50 am Thursday, June 20, 1996
Room 135

